

ELECTRONIC CIRCUIT, METHOD OF DRIVING ELECTRONIC CIRCUIT,
ELECTRONIC DEVICE, ELECTRO-OPTICAL DEVICE, METHOD OF DRIVING
ELECTRO-OPTICAL DEVICE, AND ELECTRONIC APPARATUS

Field of the Invention

[0001]

The present invention relates to an electronic circuit, a method of driving an electronic circuit, an electronic device, an electro-optical device, a method of driving an electro-optical device, and an electronic apparatus.

Background of the Invention

[0002]

It is recently expected that an electro-optical device with low power consumption, a high viewing angle, and a high contrast ratio can be realized since an organic EL device is a spontaneous emission element that can be driven by low power consumption.

[0003]

For example, one of methods of driving an electro-optical device that includes a liquid crystal element, an organic EL element, an electrophoresis element, and a field emission display (FED) is an active matrix driving method. An electro-optical device of the active matrix driving method includes a display panel with a plurality of pixel

circuits arranged in a matrix. Each of the pixel circuits includes an electro-optical element and a driving transistor for supplying driving power to the electro-optical element.

[0004]

According to the driving transistor, because the variation of characteristics of each pixel circuit such as threshold voltage, the brightness of the electro-optical device may vary in each pixel even if data signals corresponding to the same gray scale are supplied.

[0005]

In particular, when a thin film transistor is used as the driving transistor, the variation of the threshold voltage is significant. Therefore, a transistor for reducing the variation of the characteristics of the driving transistor is disposed in the pixel circuit (Patent Document 1: Japanese Unexamined Patent Application Publication No. 2001-147659).

[0006]

When a transistor for reducing the variation of the characteristics of the driving transistor is provided in each pixel circuit, the aperture ratio of the pixel circuit is reduced with the reduction in yield. For example, in the case of an organic EL element, when the aperture ratio is reduced, it is necessary to supply current as much as the reduction in the aperture ratio, thereby increasing power

consumption and reducing the life of the organic EL element.

[0007]

Accordingly, the object of the present invention is to provide an electronic circuit, a method of driving the electronic circuit, an electronic device, an electro-optical device, a method of driving the electro-optical device, and an electronic apparatus which are capable of reducing the variation of the threshold voltage while reducing the number of transistors used.

Brief Description of the Invention

[0008]

An electronic circuit of the present invention comprises a plurality of unit circuits. Each of the plurality of unit circuits comprises a first transistor including a first terminal, a second terminal, and a first control terminal; a second transistor including a third terminal and a fourth terminal, the third terminal being connected to the first terminal; an electronic element including a fifth terminal and a sixth terminal, the fifth terminal being connected to the first terminal; and a third transistor for controlling electrical connection between the first terminal and the first control terminal, wherein the sixth terminal can be set to a plurality of electric potentials or be electrically connected to a predetermined

electric potential and is electrically disconnected from the predetermined electric potential.

[0009]

Therefore, it is possible to reduce the number of transistors constituting a unit circuit as compared with a conventional art.

An electronic circuit of the present invention comprises a plurality of unit circuits. Each of the plurality of unit circuits comprises a first transistor including a first terminal, a second terminal, and a first control terminal; a second transistor including a third terminal and a fourth terminal, the third terminal being connected to the first terminal; an electronic element including a fifth terminal and a sixth terminal, the fifth terminal being connected to the first terminal; and a third transistor for controlling electrical connection between the first terminal and the first control terminal, wherein the sixth terminal includes a control circuit connected to an electric potential control line, the control circuit setting the electric potential control line to a plurality of electric potentials or controlling electrical connection and electrical disconnection between the electric potential control line and a predetermined electric potential.

[0010]

Therefore, it is possible to reduce the number of

transistors constituting a unit circuit as compared with a conventional art.

According to this electronic circuit, transistors included in each of the unit circuits being only the first transistor, the second transistor, and the third transistor.

[0011]

Therefore, it is possible to reduce the number of transistors constituting a unit circuit by one as compared with a conventional art.

According to this electronic circuit, a capacitive element is connected to the first control terminal.

[0012]

Therefore, it is possible to control the level of current that flows through an electronic element in accordance with the quantity of charge accumulated in a capacitive element.

According to this electronic circuit, the control circuit is a fourth transistor including a ninth terminal and a tenth terminal, wherein the ninth terminal is connected to the sixth terminal through the electric potential control line, and the tenth terminal is connected to a supply line for supplying the plurality of electric potentials or the predetermined electric potential.

[0013]

Therefore, it is possible to easily form the control

circuit.

According to this electronic circuit, the electronic element may be a current-driven element.

Therefore, it is possible to reduce the number of transistors constituting a unit circuit that includes a current-driven element.

[0014]

An electronic circuit of the present invention comprises an electronic element; a first transistor including a first terminal, a second terminal, and a control terminal and controlling a current level supplied to the electronic element in accordance with an electric conduction state, the first terminal being connected to one end of the electronic element; a second transistor connected to the first transistor; and a control circuit connected to another end of the electronic element, the control circuit controlling current not to flow through the electronic element in a period where current flows through a first current path including the first transistor and the second transistor and for controlling current to flow through a second current path including the first transistor and the electronic element in a state where the second transistor is in an off state.

[0015]

Therefore, it is possible to reduce the number of

transistors constituting a unit circuit.

The electronic circuit further comprises a capacitive element connected to the control terminal and holding a quantity of charge corresponding to a current level of the current flowing through the first current path.

[0016]

Therefore, it is possible to reduce the number of transistors constituting a unit circuit.

The present invention provides a method of driving an electronic circuit comprising an electronic element; a first transistor including a first terminal, a second terminal, and a control terminal, the first terminal being connected to one end of the electronic element; a capacitive element connected to the control terminal; and a second transistor connected to the first terminal. The method comprises the steps of: setting the electric potential of another end of the electronic element to an electric potential preventing current from flowing through the electronic element, supplying current to a first current path including at least the first transistor and the second transistor, and accumulating a quantity of charge corresponding to a current level of the current passing through the first current path in the capacitive element; and setting the electric potential of another end of the electronic element to an electric potential letting current flow through the

electronic element and supplying current with a current level corresponding to the quantity of charge to the electronic element.

[0017]

Therefore, it is possible to drive an electronic circuit in which the number of transistors constituting a unit circuit is reduced.

An electronic device of the present invention includes a plurality of first signal lines, a plurality of second lines, and a plurality of unit circuits, each of the plurality of unit circuits comprising: an electronic element including a first electrode and a second electrode and driven in accordance with a current level of the current flowing between the first electrode and the second electrode; a first transistor connected to the first electrode and controlling the current level in accordance with an electric conduction state; a second transistor connected to the first transistor and electrically connecting one of the plurality of second signal lines to the first transistor by switching to an on state in accordance with a control signal supplied from one of the plurality of first signal lines; and a capacitive element for holding a quantity of charge corresponding to current signals supplied from the first signal line and determining an electric conduction state of the first transistor,

wherein the electric potential of the second electrode is set such that current does not flow through the electronic element, or the second electrode is electrically disconnected from a power source potential in a period where at least the second transistor is in an on state.

[0018]

Therefore, it is possible to provide an electronic device including a plurality of unit circuits, in which the number of transistors is reduced as compared with a conventional art.

An electro-optical device of the present invention includes a plurality of scanning lines, a plurality of data lines, a plurality of unit circuits, and a plurality of power source lines, each of the plurality of unit circuits comprises: a first transistor including a first terminal, a second terminal, and a first control terminal, the second terminal being connected to one of the plurality of power source lines; a second transistor including a third terminal, a fourth terminal, and a second control terminal, the third terminal being connected to the first terminal, the fourth terminal being connected to one of the plurality of data lines, and the second control terminal being connected to one of the plurality of scanning lines; an electro-optical element including a fifth terminal and a sixth terminal, the fifth terminal being connected to the

first terminal; a capacitive element including a seventh terminal and an eighth terminal, the seventh terminal being connected to the first control terminal; a third transistor for controlling electrical connection between the first terminal and the first control terminal; an electric potential control line connected to the sixth terminal together with the sixth terminals of the other unit circuits of the plurality of unit circuits; and a control circuit for setting the electric potential control line to a plurality of electric potentials or for controlling electrical connection and electrical disconnection between the electric potential control line and a predetermined electric potential.

[0019]

Therefore, it is possible to provide an electro-optical device including a plurality of unit circuits, in which the number of transistors is reduced as compared with a conventional art. In this way, it is possible to improve the aperture ratio of the pixel circuit, thereby reducing the power consumption of the electro-optical device and reducing the current supplied to the electro-optical device. As a result, it is possible to lengthen the life of the electro-optical device.

[0020]

According to this electro-optical device, preferably,

only the first transistor, the second transistor, and the third transistor are transistors included in each of the unit circuits.

[0021]

Therefore, it is possible to provide an electro-optical device including a plurality of unit circuits, in which the number of transistors is reduced by one as compared with a conventional art.

According to this electro-optical device, the control circuit is a fourth transistor including a ninth terminal and a tenth terminal, wherein the ninth terminal is connected to the sixth terminal through the electric potential control line, and the tenth terminal is connected to a supply line for supplying the plurality of electric potentials or the predetermined electric potential.

[0022]

Therefore, it is possible to easily form the control circuit.

According to this electro-optical device, the electro-optical element is an EL element in which a light-emitting layer is made of an organic material.

[0023]

Therefore, it is possible to reduce the number of transistors in a unit circuit including an organic EL element and constituting an electro-optical device.

According to this electro-optical device, electro-optical elements of the same color are arranged along one of the plurality of scanning lines.

[0024]

Therefore, it is possible to provide an electro-optical device capable of displaying full colors, in which the number of transistors is reduced as compared with a conventional art.

The present invention provides a method of driving an electro-optical device including a plurality of data lines, a plurality of scanning lines, and a plurality of unit circuits, each of the plurality of unit circuits comprising: an electro-optical element exhibiting an optical effect in accordance with an electric potential difference between a first electrode and a second electrode; a first transistor including a first terminal, a second terminal, and a first control terminal, the first terminal being connected to the first electrode; a capacitive element connected to the first control terminal; and a second transistor including a third terminal, a fourth terminal, and a second control terminal, the third terminal being connected to the first terminal, the fourth terminal being connected to one of the plurality of data lines, and the second control terminal being connected to one of the plurality of scanning lines, the method comprising: a first step of setting an electric

potential of the second electrode such that the electro-optical element does not exhibit an optical effect and further switching the second transistor to an on state by supplying scanning signals to the second control terminal through one of the plurality of scanning lines, supplying data signals as current from the one data line to the first transistor through the second transistor, and accumulating a quantity of charge corresponding to the data signals in the capacitive element; and a second step of switching the second transistor to an off state by supplying scanning signals to the second control terminal through the scanning line and further setting an electric potential of the second electrode such that the electro-optical element exhibits an optical effect, and supplying a voltage of the voltage level or a current of the current level in accordance with an electric conduction state of the first transistor set in accordance with the quantity of charge accumulated in the capacitive element to the electro-optical element through the first electrode.

[0025]

Therefore, it is possible to drive an electro-optical device in which the number of transistors constituting a unit circuit is reduced.

According to the method of driving the electro-optical device, each of the plurality of unit circuits further

comprises a third transistor for controlling electrical connection and electrical disconnection between the first terminal and the first control terminal, wherein the first terminal is electrically connected to the first control terminal by switching the third transistor to an on state at least in a part of the period where the first step is performed, and the first terminal is electrically disconnected from the first control terminal by switching the third transistor to an off state at least in a part of the period where the second step is performed.

[0026]

Therefore, it is possible to store the quantity of charge corresponding to data signals in a capacitive element in the first step and to supply the current corresponding to the quantity of charge accumulated in the capacitive element to an electro-optical device in the second step.

[0027]

According to the method of driving the electro-optical device, the electro-optical element may be an organic EL element.

In this way, according to an electro-optical device including a unit circuit in which the number of transistors is reduced as compared with a conventional art, it is possible to drive the electro-optical device, in which the organic EL element is used as the electro-optical element

provided in the unit circuit.

[0028]

An electronic apparatus according to the present invention is mounted with the above-mentioned electronic circuit.

Therefore, according to an electronic circuit including a unit circuit that supplies current corresponding to a data signal supplied from the outside to an electronic element, it is possible to provide an electronic apparatus including an electronic circuit in which the number of transistors constituting the unit circuit is reduced by one as compared with a conventional art.

[0029]

An electronic apparatus according to the present invention is equipped with the above-mentioned electro-optical device.

Therefore, according to the electro-optical device including a unit circuit that supplies current corresponding to a data signal supplied from the outside to an electronic element, it is possible to provide an electronic apparatus including the electro-optical device in which the number of transistors constituting the unit circuit is reduced by one as compared with a conventional art. In this way, it is possible to reduce the area of the electronic circuit occupied by the transistor and thus to realize an electro-

optical device with high aperture ratio. As a result, it is possible to reduce the power consumption of the electronic apparatus and to improve the yield of the electronic apparatus.

Brief Description of the Drawings

[0030]

Fig. 1 is a circuitry block diagram illustrating a circuit structure of an organic EL display according to the present embodiments.

Fig. 2 is a circuitry block diagram illustrating internal structures of a display panel and a data line driving circuit according to a first embodiment of the present invention.

Fig. 3 is a circuit diagram of a pixel circuit according to the first embodiment of the present invention.

Fig. 4 is a timing chart illustrating a method of driving the pixel circuit according to the first embodiment of the present invention.

Fig. 5 is a circuitry block diagram illustrating internal structures of a display panel and a data line driving circuit according to a second embodiment of the present invention.

Fig. 6 is a perspective view illustrating a structure of a mobile personal computer for describing a third

embodiment of the present invention.

Detailed Description of Preferred Embodiments

(First Embodiment)

[0031]

A first embodiment of the present invention will now be described with reference to Figs. 1 to 4. Fig. 1 is a circuitry block diagram illustrating a circuit structure of an organic EL display as an electro-optical device. Fig. 2 is a circuitry block diagram illustrating internal structures of a display panel and a data line driving circuit as electronic circuits. Fig. 3 is a circuit diagram of a pixel circuit. Fig. 4 is a timing chart for describing a method of driving the pixel circuit.

[0032]

An organic EL display 10 includes a signal generating circuit 11, a display panel 12, a scanning line driving circuit 13, a data line driving circuit 14, and a power source line control circuit 15. Each of the signal generating circuit 11, the scanning line driving circuit 13, the data line driving circuit 14, and the power source line control circuit 15 of the organic EL display 10 may be formed of an independent electronic component. For example, each of the signal generating circuit 11, the scanning line driving circuit 13, the data line driving circuit 14, and

the power source line control circuit 15 may be composed of one chip semiconductor integrated circuit device. In addition, all or a part of the signal generating circuit 11, the scanning line driving circuit 13, the data line driving circuit 14, and the power source line control circuit 15 may be formed of a programmable IC chip, and the function thereof may be executed by software written in the IC chip.

[0033]

The signal generating circuit 11 generates scanning control signals and data control signals for displaying images on the display panel 12 based on image data from an external device (not shown). Furthermore, the signal generating circuit 11 outputs the scanning control signals to the scanning line driving circuit 13 and outputs the data control signals to the data line driving circuit 14. Moreover, the signal generating circuit 11 outputs timing control signals to the power source line control circuit 15.

[0034]

As illustrated in Fig. 2, the display panel 12 includes pixel circuits 20 as a plurality of unit circuits, which are arranged at positions corresponding to the intersection portions of M data lines X_m ($m = 1$ to M , where m is an integer) extending in a column direction and N scanning lines Y_n ($n = 1$ to N , where n is an integer) extending in a row direction. That is, the pixel circuits 20 are connected

between the data lines X_m extending in the column direction and the scanning lines Y_n extending in the row direction and then arranged in a matrix. The pixel circuits 20 are connected to power source lines V_{Ld} and electric potential lines L_o extending in parallel to the scanning lines Y_n .

[0035]

The power source lines V_{Ld} are connected to a first voltage supply line L_a extending along the column direction of the pixel circuits 20 arranged at the right end of the display panel 12. The first voltage supply line L_a is connected to a power source (not shown) for supplying a driving voltage V_{dd} . Therefore, the driving voltage V_{dd} is supplied to the respective pixel circuits 20 through the first voltage supply line L_a and the power source lines V_{Ld} .

[0036]

The electric potential control lines L_o are connected to control circuits TS . The control circuits TS are connected to a second voltage supply line L_b extending along the column direction of the pixel circuits 20 arranged at the right end of the display panel 12. The second voltage supply line L_b is connected to the power source (not shown) for supplying a cathode voltage V_o . Furthermore, The control circuits TS are connected to the power source line control circuit 15 which supplies power source line control signals SC_n (which is mentioned later) for controlling the

control circuits TS through power source line control lines F. The driving voltage V_{dd} is previously set to be larger than the cathode voltage V_o .

[0037]

As illustrated in Fig. 2, the pixel circuits 20 include organic EL elements 21 in which light-emitting layers are formed of an organic material. Transistors (which are mentioned later) arranged in the pixel circuits 20 are generally formed of TFTs (thin film transistors).

The scanning line driving circuit 13 selects one scanning line of N scanning lines Y_n arranged in the display panel 12 based on the scanning control signals output from the signal generating circuit 11 and outputs scanning signals SY_1 , SY_2 , ..., and SY_n to the selected scanning line.

[0038]

The data line driving circuit 14 includes a plurality of single line drivers 23 as illustrated in Fig. 2. The single line drivers 23 are connected to the corresponding data lines X_m arranged in the display panel 12. The data line driving circuit 14 generates data currents I_{data1} , I_{data2} , ..., and I_{dataM} based on the data control signals output from the signal generating circuit 11. The data line driving circuit 14 outputs the generated data currents I_{data1} , I_{data2} , ..., and I_{dataM} to the pixel circuits 20

through the data lines X_m . When the internal states of the pixel circuits 20 are set in accordance with the data currents I_{data1} , I_{data2} , ..., and I_{dataM} , the pixel circuits 20 control driving current I_{el} supplied to the organic EL elements 21 in accordance with the current levels of the data currents I_{data1} , I_{data2} , ..., and I_{dataM} .

[0039]

The power source line control circuit 15 is connected to the control circuits TS through the power source line control lines F as mentioned above. The power source line control circuit 15 generates the power source line control signals SC_n for determining electrical connection state (an on state) or electrical disconnection state (an off state) between the electric potential control lines L_o and the first voltage supply line L_a based on the timing control signals output from the signal generating circuit 11. Furthermore, the power source line control circuit 15 generates the power source line control signals SC_n for determining the electrical connection state (the on state) or the electrical disconnection state (the off state) between the electric potential control lines L_o and the second voltage supply line L_b based on the timing control signals output from the signal generating circuit 11.

[0040]

To be specific, the power source line control signals

SCn electrically disconnect the second voltage supply line Lb from the electric potential control lines Lo (the off state) when the electric potential control lines Lo are electrically connected to the first voltage supply line La (the on state) and electrically connect the electric potential control lines Lo to the second voltage supply line Lb (the on state) when the electric potential control lines Lo are electrically disconnected from the first voltage supply line La (the off state).

[0041]

The control circuits TS supply the driving voltage Vdd or the cathode voltage Vo to the pixel circuits 20 through the electric potential control lines Lo in response to the power source line control signals SCn.

[0042]

The pixel circuits 20 of the organic EL display 10 constituted as described above will now be described with reference to Fig. 3. For the convenience of description, the pixel circuits 20 arranged between the scanning lines Yn and the data lines Xm will now be described.

[0043]

As illustrated in Fig. 3, the pixel circuit 20 includes three transistors, a capacitive element, and an organic EL element 21. To be specific, the pixel circuit 20 includes a driving transistor Qd, a first switching transistor Qs1, a

second switching transistor Qs2, and a storage capacitor Co. The conductive type of the driving transistor Qd is a p type (a p channel). The conductive types of the first and second switching transistors Qs1 and Qs2 are an n type (an n channel).

[0044]

A source of the driving transistor Qd is connected to the power source line VLd. A drain of the driving transistor Qd is connected to a source of the first switching transistor Qs1 and a first electrode E1 of the organic EL element 21.

[0045]

Furthermore, the second switching transistor Qs2 is connected between a gate of the driving transistor Qd and the drain of the driving transistor Qd. A first electrode D1 of the storage capacitor Co is connected to the gate of the driving transistor Qd. A second electrode D2 of the storage capacitor Co is connected to the power source line VLd.

[0046]

A drain of the first switching transistor Qs1 is connected to the data line Xm. A gate of the first switching transistor Qs1 is connected to a gate of the second switching transistor Qs2 and the scanning line Yn. A second electrode E2 of the organic EL element 21 is

connected to the electric potential line Lo.

[0047]

The control circuit TS is connected to the electric potential control line Lo connected to the pixel circuit 20 with the above structure. The control circuit TS is arranged between the first and second voltage supply lines La and Lb and the pixel circuit 20 arranged along the rightmost column direction of the pixel circuits 20 arranged in the display panel 12 in a matrix.

[0048]

The control circuit TS includes a cathode voltage transistor Qo and a driving voltage transistor QDD. The conductive type of the cathode voltage transistor Qo is an n type (an n channel). The conductive type of the driving voltage transistor QDD is a p type (a p channel).

[0049]

A source of the cathode voltage transistor Qo is connected to a drain of the driving voltage transistor QDD and the electric potential control line Lo. A drain of the cathode voltage transistor Qo is connected to the second voltage supply line Lb for supplying the cathode voltage Vo. A source of the driving voltage transistor QDD is connected to the first voltage supply line La for supplying the driving voltage Vdd. A gate of the cathode voltage transistor Qo and a gate of the driving voltage transistor

QDD are connected to each other and are connected to the power source line control line F. Furthermore, the power source line control signals SCn generated by the power source line control circuit 15 are supplied to the gate of the cathode voltage transistor Qo and the gate of the driving voltage transistor QDD.

[0050]

That is, the control circuits TS are shared by the pixel circuits 20 arranged in the display panel 12 in the row direction.

According to the present embodiment, a first transistor, a second transistor, and a third transistor described in the claims correspond to, for example, the driving transistor Qd, the first switching transistor Qs1, and the second switching transistor Qs2, respectively. According to the present embodiment, a first terminal and a second terminal described in the claims correspond to, for example, the drain of the driving transistor Qd and the source of the driving transistor Qd, respectively. Furthermore, according to the present embodiment, a first control terminal or a control terminal of the first transistor described in the claims corresponds to, for example, the gate of the driving transistor Qd.

[0051]

According to the present embodiment, a third terminal,

a fourth terminal, and a second control terminal described in the claims correspond to, for example, the drain of the first switching transistor Qs1, the source of the first switching transistor Qs1, and the gate of the first switching transistor Qs1, respectively. Furthermore, according to the present embodiment, a fifth terminal and a sixth terminal described in the claims correspond to, for example, the first electrode E1 and the second electrode E2 of the organic EL element 21, respectively. Moreover, according to the present embodiment, a fourth transistor described in the claims corresponds to, for example, the cathode voltage transistor Qo or the driving voltage transistor QDD.

[0052]

According to the organic EL display 10 with the above-mentioned structure, when the driving voltage transistor QDD is in the electrical connection state (the on state) in accordance with the power source line control signals SCn, the driving voltage Vdd is supplied to the second electrode E2 of the organic EL element 21 through the electric potential control line Lo. Therefore, the second electrode E2 of the organic EL element 21 becomes an H state.

The driving voltage Vdd supplied to the second electrode E2 functions as an electric potential in which the organic EL element 21 does not emit light.

[0053]

At this time, since driving voltage V_{dd} is supplied to the first electrode E1 of the organic EL element 21, current does not flow through the organic EL element 21. Therefore, the organic EL element 21 does not emit light.

[0054]

Furthermore, when the cathode voltage transistor Q_0 becomes the electrical connection state (the on state) in accordance with the power source line control signals SC_n , the cathode voltage V_0 is supplied to the second electrode E2 of the organic EL element 21 through the electric potential control line Lo . A forward bias is supplied to the organic EL element 21 since the cathode voltage V_0 is set to be smaller than the driving voltage V_{dd} . As a result, the driving current I_{el} received from the driving transistor Q_d is supplied to the organic EL element 21. Therefore, the brightness of the organic EL element 21 is determined in accordance with the current level of the driving current I_{el} .

[0055]

Next, a method of driving the pixel circuits 20 of the organic EL display 10 with the above-mentioned structure will now be described with reference to Fig. 4. In Fig. 4, a driving period T_c is a period in which the brightness of the organic EL element 21 is updated once. The driving

period T_c is the same as a frame period. T_1 denotes a data-writing period. T_2 denotes a light-emitting period. The driving period T_c includes the data-writing period T_1 and the light-emitting period T_2 .

[0056]

In the pixel circuit 20, The scanning signals SY_n for switching the first and second switching transistors Q_{s1} and Q_{s2} to the on state for the writing period T_1 are supplied from the scanning line driving circuit 13 through the scanning lines Y_n . At this time, the power source line control signals SC_n for switching the cathode voltage transistor Q_o to the off state are supplied from the power source line control circuit 15 to the gate of the cathode voltage transistor Q_o through the power source line control line F .

[0057]

In this way, the first and second switching transistors Q_{s1} and Q_{s2} become the on state. Thus, the data current I_{dataM} is supplied to the storage capacitor C_o through the first switching transistor Q_{s1} and the second switching transistor Q_{s2} . As a result, the voltage V_o corresponding to the quantity of charge in accordance with the current level of the data current I_{dataM} is held in the storage capacitor C_o . At this time, the variation in the characteristics of the driving transistor Q_d , such as a

threshold voltage and mobility, is compensated for since the driving transistor Qd is previously set to operate in a saturation region.

[0058]

At this time, the power source line control signals SCn for switching the driving voltage transistor QDD to the on state are supplied from the power source line control circuit 15 to the control circuit TS, and then, the driving voltage transistor QDD becomes the on state. As a result, the driving voltage Vdd is supplied to the second electrode E2 of the organic EL element 21.

[0059].

Therefore, since the electric potential of the second electrode E2 of the organic EL element 21 is equal to the driving voltage Vdd as illustrated in Fig. 4, the organic EL element 21 becomes a non-order bias state or a reverse bias state. As a result, the organic EL element 21 does not emit light.

[0060]

The scanning signals SYn for switching the first switching transistor Qs1 and the second switching transistor Qs2 to the off state are supplied from the scanning line driving circuit 13 through the scanning lines Yn for the light-emitting period T2 after the data writing period T1. In this way, the first switching transistor Qs1 and the

second switching transistor Qs2 become the off state.

[0061]

At this time, the power source line control signals SCn for switching the cathode voltage transistor Qo to the on state are supplied from the power source line control circuit 15 to the control circuit TS. Therefore, the cathode voltage transistor Qo becomes the on state. As a result, the cathode voltage Vo is supplied to the second electrode E2 of the organic EL element 21, and thus the second electrode E2 of the organic EL element 21 becomes an L state.

[0062]

That is, as illustrated in Fig. 4, since the electric potential of the second electrode E2 of the organic EL element 21 is the cathode voltage Vo and the electric potential of the second electrode E2 is lower than that of the first electrode E1, the forward bias is supplied to the organic EL element 21.

[0063]

As a result, the driving current Iel corresponding to the voltage Vo held in the storage capacitor Co for the data-writing period T1 flows through the organic EL element 21. Therefore, the brightness gradation of the organic EL element 21 is precisely controlled in accordance with the data current IdataM.

[0064]

As mentioned above, for the pixel circuit 20, it is possible to reduce the number of transistors provided therein by one as compared with the conventional art and to control the brightness gradation of the organic EL element 21 with high precision in accordance with the data current I_{dataM} . Therefore, for the pixel circuit 20, it is possible to improve an aperture ratio or yield in manufacturing the organic EL display 10.

[0065]

According to the electronic circuit and the electro-optical device of the present embodiment, it is possible to obtain the following characteristics.

(1) According to the present embodiment, each of the pixel circuits 20 consists of the driving transistor Qd, the first switching transistor Qs1, the second switching transistor Qs2, the storage capacitor Co, and the organic EL element 21.

Each of the control circuits TS connected to the second electrode E2 of the organic EL element 21 through the electric potential line Lo and setting the electric potential of the second electrode E2 to the driving voltage Vdd or the cathode voltage Vo is provided in each of the plurality of pixel circuits 20.

[0066]

In this way, for the pixel circuit 20, it is possible to reduce the number of transistors provided therein by one as compared with the conventional pixel circuit while compensating for the variation in the threshold voltage or the mobility of the driving transistor Qd. As a result, for the pixel circuit 20 of the organic EL display 10, it is possible to improve the yield or the aperture ratio in manufacturing the transistors in addition to controlling the brightness gradation of the organic EL element 21 with high precision.

(Second Embodiment)

A second embodiment according to the present invention will now be described with reference to Fig. 5. In the present embodiment, the same members as those of the first embodiment are denoted by the same reference numerals, and the detailed description thereof will be omitted.

[0067]

Fig. 5 is a circuitry block diagram illustrating internal structures of a display panel 12a and a data line driving circuit 14 of the organic EL display 10. According to the present embodiment, the display panel 12a includes pixel circuits for red 20R having organic EL elements 21 that emit red light, pixel circuits for green 20G having organic EL elements 21 that emit green light, and pixel circuits for blue 20B having organic EL elements 21 that

emit blue light. The structures of the pixel circuits for red, green, and blue 20R, 20G, and 20B are the same as those of the pixel circuits 20 according to the first embodiment.

[0068]

To be specific, in the display panel 12a, the pixel circuits for red, green, and blue 20R, 20G, and 20B are arranged along the direction of the scanning lines Y_n . The driving transistor Q_d and the storage capacitor C_o constituting the pixel circuit for red 20R are connected to a first voltage supply line for red La_R for supplying a driving voltage for red V_{ddR} through the power source line VL_d . The driving transistor Q_d and the storage capacitor C_o constituting the pixel circuit for green 20G are connected to a first voltage supply line for green La_G for supplying a driving voltage for green V_{ddG} through the power source line VL_d . The driving transistor Q_d and the storage capacitor C_o constituting the pixel circuit for blue 20B are connected to a first voltage supply line for blue La_B for supplying a driving voltage for blue V_{ddB} through the power source line VL_d .

[0069]

The driving voltages for red, green, and blue V_{ddR} , V_{ddG} , and V_{ddB} are the driving voltage of the driving transistor Q_d constituting the pixel circuit for red 20R, the driving voltage of the driving transistor Q_d

constituting the pixel circuit for green 20G, and the driving voltage of the driving transistor Qd constituting the pixel circuit for blue 20B, respectively.

[0070]

Next, a method of driving the pixel circuits 20R, 20G, and 20B of the organic EL display 10 with the above-mentioned structure will now be described.

First, a first scanning signal SY1 for switching the first and second switching transistors Qs1 and Qs2 of the pixel circuits for red 20R to the on state respectively is supplied from the scanning line driving circuit 13 through the first scanning line Y1. Furthermore, the power source line control signals SCn for switching the driving voltage transistors QDD to the on states are supplied from the power source line control circuit 15 through the electric potential control lines Lo.

[0071]

As a result, in the pixel circuit for red 20R arranged in a direction where the first scanning line Y1 extends, the first and second switching transistors Qs1 and Qs2 connected to the first scanning line Y1 become the on state, respectively, and the electric potential of the second electrode E2 of the organic EL element for red 21 becomes the driving voltage Vdd.

In this state, the data current Idata is supplied from

the data line X_m to the storage capacitor C_o through the first switching transistor Q_{s1} and the second switching transistor Q_{s2} . As a result, the voltage V_o corresponding to the quantity of charge in accordance with the current level of the data current I_{dataM} is stored in the storage capacitor C_o .

[0072]

Subsequently, the first scanning signal SY_1 for switching the first and second switching transistors Q_{s1} and Q_{s2} of the pixel circuit for red 20R to the off state respectively is supplied from the scanning line driving circuit 13 through the first scanning line Y_1 . Furthermore, the power source line control signal SC_n for switching the cathode voltage transistor Q_o to the on state is supplied from the power source line control circuit 15 through the electric potential control line Lo .

[0073]

As a result, in the pixel circuit for red 20R, the first and second switching transistors Q_{s1} and Q_{s2} connected to the first scanning line Y_1 become the off state, respectively, and the electric potential of the second electrode E_2 of the organic EL element for red 21 becomes the cathode voltage V_o . Therefore, since the forward bias is supplied to the organic EL element for red 21, the driving current I_{el} is supplied to the organic EL element

for red 21, and the organic EL element for red 21 emits light.

[0074]

The second scanning signal SY2 for switching the first and second switching transistors Qs1 and Qs2 of the pixel circuit for green 20G to the on state is supplied from the scanning line driving circuit 13 through the second scanning line Y2. Furthermore, the power source line control signal SCn for switching the driving voltage transistor QDD to the on state is supplied from the power source line control circuit 15 through the electric potential control line Lo.

[0075]

As a result, in the pixel circuit for green 20G arranged in a direction where the second scanning line Y2 extends, the first and second switching transistors Qs1 and Qs2 connected to the second scanning line Y2 become the on state, respectively, and the electric potential of the second electrode E2 of the organic EL element for green 21 becomes the driving voltage Vdd. The data current Idata is supplied from the data line Xm to the storage capacitor Co through the first switching transistor Qs1 and the second switching transistor Qs2. As a result, the voltage Vo corresponding to the quantity of charge in accordance with the current level of the data current IdataM is held in the storage capacitor Co.

[0076]

Subsequently, the second scanning signal SY2 for switching the first and second switching transistors Qs1 and Qs2 of the pixel circuit for green 20G to the off state respectively is supplied from the scanning line driving circuit 13 through the second scanning line Y2.

Furthermore, the power source line control signal SCn for switching the driving voltage transistor QDD to the on state is supplied from the power source line control circuit 15 through the electric potential control line Lo.

[0077]

As a result, in the pixel circuit for green 20G, the first and second switching transistors Qs1 and Qs2 connected to the second scanning line Y2 become the off state, respectively, and the electric potential of the second electrode E2 of the organic EL element for green 21 becomes the cathode voltage Vo. Therefore, since the forward bias is supplied to the organic EL element for green 21, the driving current Iel is supplied to the organic EL element for green 21, and the organic EL element for green 21 emits light.

[0078]

Furthermore, a third scanning signal SY3 for switching the first and second switching transistors Qs1 and Qs2 of the pixel circuit for blue 20B to the on state respectively

is supplied from the scanning line driving circuit 13 through a third scanning line Y3. Moreover, The power source line control signal SCn for switching the cathode voltage transistor Qo to the on state is supplied from the power source line control circuit 15 through the electric potential control line Lo.

[0079]

As a result, in the pixel circuit for blue 20B arranged in a direction where the third scanning line Y3 extends, the first and second switching transistors Qs1 and Qs2 connected to the third scanning line Y3 become the on state, and the electric potential of the second electrode E2 of the organic EL element for blue 21 becomes the driving voltage Vdd. In this state, the data current Idata is supplied from the data line Xm to the storage capacitor Co through the first switching transistor Qs1 and the second switching transistor Qs2. As a result, the voltage Vo corresponding to the quantity of charge in accordance with the current level of the data current IdataM is held in the storage capacitor Co.

[0080]

Subsequently, the third scanning signal SY3 for switching the first and second switching transistors Qs1 and Qs2 of the pixel circuit for blue 20B to the off state is supplied from the scanning line driving circuit 13 through the third scanning line Y3. Furthermore, the power source

line control signal SCn for switching the driving voltage transistor QDD to the on state is supplied from the power source line control circuit 15 through the electric potential control line Lo.

[0081]

As a result, in the pixel circuit for blue 20G, the first and second switching transistors Qs1 and Qs2 connected to the third scanning line Y3 become the off state, and the electric potential of the second electrode E2 of the organic EL element for blue 21 becomes the cathode voltage Vo. Therefore, since the forward bias is supplied to the organic EL element for blue 21, the driving current Iel is supplied to the organic EL element for blue 21, and the organic EL element for blue 21 emits light.

[0082]

Therefore, it is possible to obtain the same effect as that of the first embodiment from the organic EL display 10 of the second embodiment.

(Third Embodiment)

Next, an embodiment in which the organic EL displays 10 as the electro-optical devices described in the first and second embodiments are applied to electronic apparatuses will be described with reference to Fig. 6. The organic EL display 10 can be applied to various electronic apparatuses, such as a mobile personal computer, a mobile telephone, and

a digital camera.

[0083]

Fig. 6 is a perspective view illustrating the structure of a mobile personal computer. In Fig. 6, a mobile personal computer 70 includes a main body 72 including a keyboard 71 and a display unit 73 using the organic EL display 10.

The display unit 73 using the organic EL display 10 provides the same effects as those of the first embodiment. As a result, it is possible to provide the mobile personal computer 70 including the organic EL display 10 capable of controlling the brightness gradation of the organic EL element 21 with high precision and of improving yield or an aperture ratio.

[0084]

Furthermore, the embodiment of the present invention is not limited to the above embodiments and may vary as follows.

- According to the above embodiments, the electric potential supplied to the second electrode E2 of the organic EL element 21 is the driving voltage Vdd so that the organic EL element 21 does not exhibit the optical effect thereof. However, the electric potential is not limited thereto and any electric potential, by which the organic EL element 21 does not exhibit the optical effect thereof, is preferable. The second electrode E2 may be a floating electrode.

[0085]

• According to the above embodiments, the plurality of power source lines VLd and the plurality of electric potential control lines Lo are connected to the first voltage supply line La. Alternatively, the plurality of first voltage supply lines La may be provided. The first voltage supply lines La are divided into the first voltage supply lines La connected to the plurality of power source lines VLd and the first voltage supply lines La connected to the plurality of electric potential control lines Lo. In this way, variation in the electric potential of the second electrode D2 of the storage capacitor Co due to the power source line control signal SCn is reduced. Therefore, it is possible to stably control the brightness of the organic EL element 21 in addition to the effects of the above-mentioned embodiments.

[0086]

• According to the above embodiments, one control circuit TS is shared by the plurality of pixel circuits 20 arranged along one scanning line Yn. Alternatively, one control circuit TS may be shared by the plurality of pixel circuits 20 arranged along one data line Xm (or a group of data lines). In this case, the data current Idata is supplied to the pixel circuits 20 arranged along the data line Xm in a state where the driving voltage transistor QDD

constituting the control circuit TS is in the on state. Then, the organic EL elements 21 of the pixel circuits 20 simultaneously emit light by switching the cathode voltage transistor Q₀ constituting the control circuit TS to the on state.

The control circuit TS may be shared by the plurality of pixel circuits 20 arranged along the plurality of scanning lines.

In this way, it is possible to obtain the same effects as those of the above embodiments.

[0087]

- According to the above embodiments, the source of the driving voltage transistor QDD is connected to the first voltage supply line for supplying the driving voltage V_{dd}. When the optical effect of the organic EL element 21 does not exhibit, the electric potential of the second electrode E2 of the organic EL element 21 is made equal to that of the first electrode E1 by supplying the driving voltage V_{dd} to the second electrode E2 of the organic EL element 21 through the first voltage supply line. As a result, the driving current I_{el} does not flow through the organic EL element 21.

[0088]

Alternatively, the source of the driving voltage transistor QDD may be connected to the voltage supply line for supplying a voltage no less than the driving voltage

Vdd. When the optical effect of the organic EL element 21 does not exhibit, the electric potential of the second electrode E2 of the organic EL element 21 may be made larger than that of the first electrode E1 by supplying the voltage no less than the driving voltage Vdd to the second electrode E2 of the organic EL element 21 through the voltage supply line, and the driving current Iel does not flow through the organic EL element 21. Thus, it is possible to obtain the same effects as those of the above embodiments.

[0089]

- According to the above embodiments, the conductive type of the driving transistor Qd of the pixel circuit 20 is the p type (the p channel). Furthermore, the conductive types of the first switching transistor Qs1 and the second switching transistor Qs2 are set to the n type (the n channel). The drain of the driving transistor Qd is connected to the anode of the organic EL element, and the second electrode E2 of the organic EL element is connected to the electric potential control line Lo.

[0090]

Alternatively, the conductive type of the driving transistor Qd may be set to the n type, and the conductive types of the first switching transistor Qs1 and the second switching transistor Qs2 may be set to the p type (the p channel).

In this case, the source of the driving transistor Qd arranged as mentioned above may be connected to the cathode of the organic EL element, and the anode of the organic EL element may be connected to the electric potential control line Lo. It is possible to apply the pixel circuit 20 to the pixel circuit of an electro-optical device of a top emission method by constituting the pixel circuit 20 as mentioned above.

[0091]

- According to the above embodiments, the gate of the first switching transistor Qs1 is connected to the gate of the second switching transistor Qs2 and to the scanning line Yn. Alternatively, the gate of the first switching transistor Qs1 and the gate of the second switching transistor Qs2 may be separately connected to scanning lines.

[0092]

- According to the above embodiments, the control circuit Ts comprises the driving voltage transistor QDD and the cathode voltage transistor Qo. Alternatively, the control circuit TS may comprise a switch capable of switching between a low electric potential and a high electric potential instead of the driving voltage transistor QDD and the cathode voltage transistor Qo.

Furthermore, a buffer circuit or a voltage follower

circuit including a source follower circuit may be used in order to improve the driving ability of the driving voltage transistor QDD and the cathode voltage transistor Qo.

According to such constitution, it is possible to obtain the same effects as those of the above embodiments.

- According to the above embodiments, the non-order bias or the reverse bias is applied to the organic EL element 21, which is an electronic element, during the writing of data. However, for example, it is possible to set a period for applying the non-order bias or the reverse bias in addition to the period for writing data in order to lengthen the life of the organic EL element 21.

[0093]

According to the above embodiments, the first and second voltage supply lines La and Lb are provided at the right end of the display panel 12, but not necessarily. The first and second voltage supply lines La and Lb may be provided at the left end of the display panel 12. In this way, it is possible to obtain the same effects as those of the above embodiments.

[0094]

- According to the above embodiments, appropriate effects are obtained by applying the present invention to the pixel circuit 20 as the unit circuit. However, the present invention may be applied to a unit circuit for

driving an electro-optical element such as a LED or a FED other than the organic EL element 21. Furthermore, the present invention may be applied to a memory device such as a RAM (in particular, a MRAM).

[0095]

- According to the above embodiments, the present invention is applied to the organic EL element 21 as a current-driven element of the pixel circuit 20. However, the present invention may be applied to an inorganic EL element. That is, the present invention may be applied to an inorganic EL display formed of an inorganic EL element.